

**UTTARAKHAND RESIDENTIAL UNIVERSITY ALMORA**

**B. Sc. (Cyber Security)- Sem II**

S No	Roll No	Enrolment No	Name	Father's Name	Basics of Electronics				Engineering Design				Technical Diagram			Grant Total	%
					External	Internal	Practical	Total	External	Internal	Practical	Total	External	Internal	Total		
					75	25	50	150	75	25	50	150	75	25	100		
1	BSCCC019	20171119201	Chandan Singh Bisht	Rajendra Singh Bisht	25	18	42	85	29	18	46	93	26	16	42	220	55.00
2	BSCCC005	20171119202	Chandra Shekhar Pandey	Basant Ballabh Pandey	25	16	44	85	42	21	45	108	54	20	74	267	66.75
3	BSCCC24	20171119203	Gagan Patwal	Bhupal Singh Patwal	41	22	38	101	52	20	45	117	34	18	52	270	67.50
4	BSCCC003	20171119204	Jagdish Narayan Singh	Anand Singh Kumayan	26	15	36	77	32	17	39	88	31	16	47	212	53.00
5	BSCCC013	20171119205	Kiran Mehta	Kundan Singh Mehta	40	19	35	94	51	21	45	117	34	16	50	261	65.25
6	BSCCC004	20171119206	Neha Bora	Thakur Singh Bora	39	19	35	93	56	16	43	115	41	15	56	264	66.00
7	BSCCC014	20171119207	Piyush Negi	Surendra Singh Negi	27	16	35	78	21	19	38	78	32	16	48	204	51.00
8	BSCCC028	20171119208	Sagar Joshi	Jagdish Chandra Joshi	51	20	44	115	31	20	43	94	35	16	51	260	65.00
9	BSCCC007	20171119209	Saurabh Tiwari	Ramesh Chandra Tiwari	36	17	38	91	53	17	46	116	31	16	47	254	63.50
10	BSCCC030	20171119210	Shivansh Sharma	Rajendra Sharma	28	20	44	92	29	18	43	90	21	16	37	219	54.75
11	BSCCC029	20171119211	Sourav Kimtwal	Bhupal Kimtwal	40	19	36	95	27	17	39	83	35	16	51	229	57.25
12	BSCCC032	20171119212	Tarun Bhatt	Devki Nandan Bhatt	50	18	44	112	47	19	45	111	40	18	58	281	70.25
13	BSCCC031	20171119213	Virendra Singh Kaira	Vijay Singh Kaira	70	17	44	131	58	20	41	119	48	19	67	317	79.25

Note : Boxes encircled with red colour represent that the candidate has failed in that paper.